

Confirmation No. 6633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	LETAVIC	Examiner:	Kebede, B.
Serial No.:	10/560,573	Group Art Unit:	2823
Filed:	December 12, 2005	Docket No.:	US030162US2
Title:	High-Side CMOS in an Integrated SOI Process		

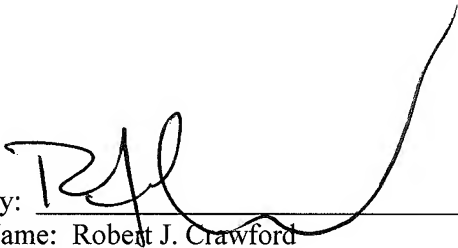
MAIL STOP RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No. 65913

1. This is a request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.
2. Attached please find:
 - a. ☒ An Amendment with Request for Continued Examination, submitted herewith to advance the above-referenced patent application under a final rejection dated 10/31/2007.
3. Fees:
 - a. ☒ Please charge Deposit Account Number 50-0996 (NXPS.246PA) the below RCE filing fee, with authorization to charge/credit said deposit account any additional required claims fees/overages or extension of time to complete this filing.
 1. ☒ **The RCE filing fee of \$810.00.**

Please direct all correspondence to:

Corporate Patent Counsel
NXP Intellectual Property & Standards
1109 McKay Drive; Mail Stop S41-SJ
San Jose, CA 95131
Customer No. 65913

By: 
Name: Robert J. Crawford
Reg. No.: 32,122
(NXPS.246PA)